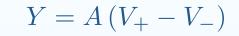
- Operational Amplifier
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- Summary

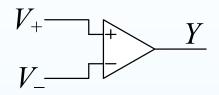
# **6: Operational Amplifiers**

#### 6: Operational Amplifiers

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An op amp (operational amplifier) is a circuit with two inputs and one output.



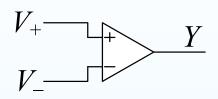


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 $Y = A\left(V_{+} - V_{-}\right)$ 



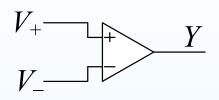
The gain, A, is usually very large: e.g.  $A = 10^5$  at low frequencies.

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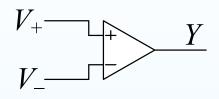
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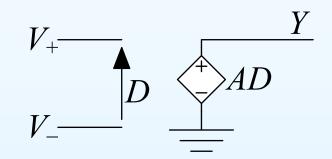
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Internally it is a complicated circuit with about 40 components, but we can forget about that and treat it as an almost perfect dependent voltage source.

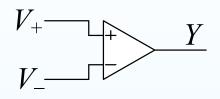


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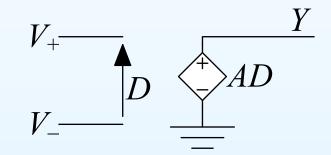
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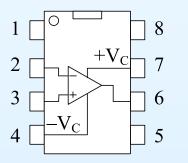
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Integrated circuit pins are numbered anti-clockwise from blob or notch (when looking from above).





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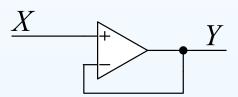
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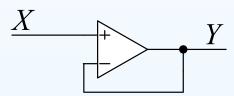
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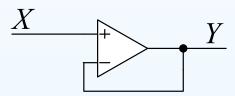
 $Y\left(1+A\right) = AX$ 

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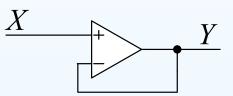


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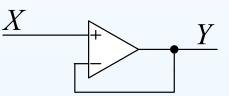
 $\begin{array}{ll} Y = A \left( X - Y \right) \\ Y \left( 1 + A \right) = A X \quad \Rightarrow \quad Y = \frac{1}{1 + \frac{1}{A}} X \quad \rightarrow X \text{ for large } A \end{array}$ 

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Y = A (X - Y) $Y (1 + A) = AX \implies Y = \frac{1}{1 + \frac{1}{A}}X \longrightarrow X \text{ for large } A$ 

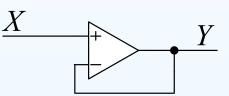
If  $Y = A(V_+ - V_-)$  then  $V_+ - V_- = \frac{Y}{A}$  which, since  $A \simeq 10^5$ , is normally *very very* small.

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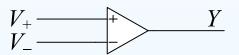
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Golden Rule: Negative feedback adjusts the output to make  $V_+ \simeq V_-$ .

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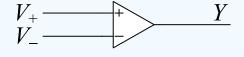
Nodal analysis is simplified by making some assumptions.



1. Check for negative feedback: to ensure that an increase in Y makes  $(V_+ - V_-)$  decrease, Y must be connected (usually via other components) to  $V_-$ .

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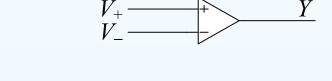
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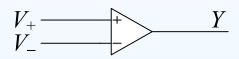


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Nodal analysis is simplified by making some assumptions.

Note: The op-amp needs two power supply connections; usually +15 V and -15 V. These are almost always omitted from the circuit diagram. The currents only sum to zero (KCL) if all five connections are included.

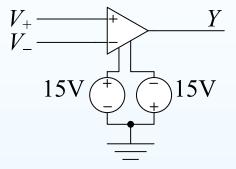


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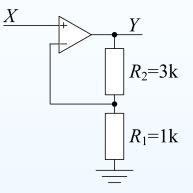


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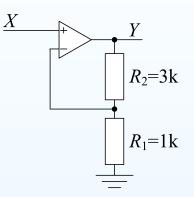
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Circuit has input voltage X and output voltage Y. The circuit gain  $\triangleq \frac{Y}{X}$ .

Applying steps 1 to 3:

1. Negative feedback OK.



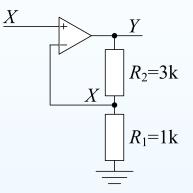
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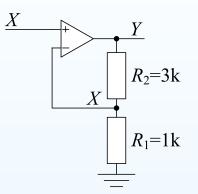


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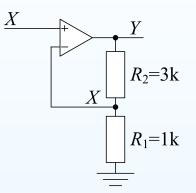
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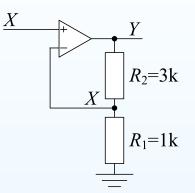
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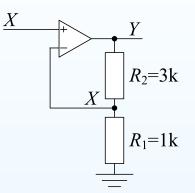
*Non-inverting amplifier* because the gain  $\frac{Y}{X}$  is positive. Consequence of X connecting to  $V_+$  input. Can have any gain  $\geq 1$  by choosing the ratio  $\frac{R_2}{R_1}$ .

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3. Zero input current at  $V_{-}$  means  $R_{2}$  and  $R_{1}$  are in series ( $\Rightarrow$  same current) and form a voltage divider. So  $X = \frac{R_{1}}{R_{1}+R_{2}}Y$ . So  $Y = \frac{R_{1}+R_{2}}{R_{1}}X = \left(1 + \frac{R_{2}}{R_{1}}\right)X = +4X$ .

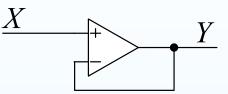
*Non-inverting amplifier* because the gain  $\frac{Y}{X}$  is positive. Consequence of *X* connecting to  $V_+$  input. Can have any gain  $\geq 1$  by choosing the ratio  $\frac{R_2}{R_1}$ .

Cause/effect reversal: Potential divider causes  $V_{-} = \frac{1}{4}Y$ . Feedback inverts this so that  $Y = 4V_{+}$ .

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- Summary

A special case of the non-inverting amplifier with  $R_1 = \infty$  and/or  $R_2 = 0$ .

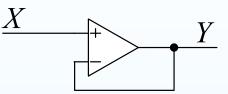


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A special case of the non-inverting amplifier with  $R_1 = \infty$  and/or  $R_2 = 0$ .

Gain is  $1 + \frac{R_2}{R_1} = 1$ .

Output Y "follows" input X.

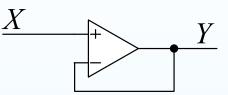


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.

 ${\rm Output}\ Y\ ``{\rm follows"\ input}\ X.$ 



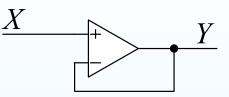
Advantage: Can supply a large current at Y while drawing almost no current from X. Useful if the source supplying X has a high resistance.

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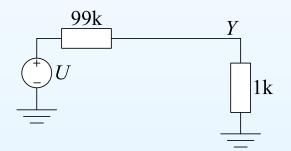
Gain is 
$$1 + \frac{R_2}{R_1} = 1$$
.

 ${\rm Output}\ Y \ ``{\rm follows"}\ {\rm input}\ X.$ 



Advantage: Can supply a large current at Y while drawing almost no current from X. Useful if the source supplying X has a high resistance.

Without voltage follower: Y = 0.01U.

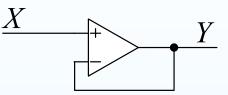


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Gain is  $1 + \frac{R_2}{R_1} = 1$ .

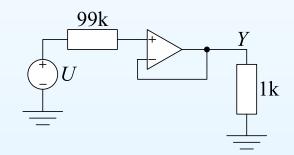
Output Y "follows" input X.



Advantage: Can supply a large current at Y while drawing almost no current from X. Useful if the source supplying X has a high resistance.

Without voltage follower: Y = 0.01U.

With voltage follower: Y = U.

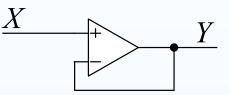


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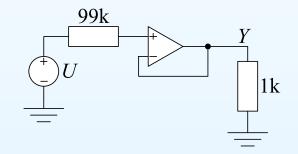
Output Y "follows" input X.



Advantage: Can supply a large current at Y while drawing almost no current from X. Useful if the source supplying X has a high resistance.

Without voltage follower: Y = 0.01U.

With voltage follower: Y = U.



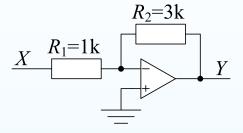
Although the *voltage gain* is only 1, the power gain is much larger.

## **Inverting Amplifier**

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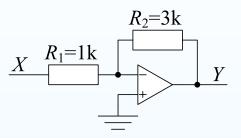
#### Negative feedback OK.



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Negative feedback OK.

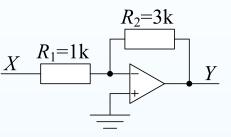
Since  $V_+ = 0$ , we must have  $V_- = 0$ .



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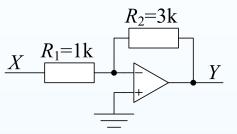


KCL at 
$$V_{-}$$
 node:  $\frac{0-X}{R_{1}} + \frac{0-Y}{R_{2}} = 0 \implies Y = -\frac{R_{2}}{R_{1}}X = -3X.$ 

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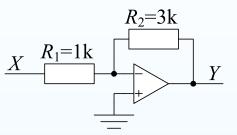
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*Inverting Amplifier* because gain  $\frac{Y}{X}$  is negative. Consequence of X connecting to the  $V_{-}$  input (via  $R_{1}$ ). Can have any gain  $\leq 0$  by choosing the ratio  $\frac{R_{2}}{R_{1}}$ .

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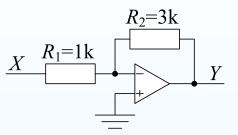
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Negative feedback holds  $V_{-}$  very close to  $V_{+}$ . If  $V_{+} = 0$  V, then  $V_{-}$  is called a *virtual earth* or *virtual ground*.

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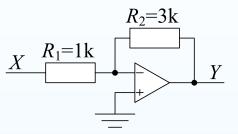
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Nodal Analysis: Do KCL at  $V_+$  and/or  $V_-$  to solve circuit. When analysing a circuit, you never do KCL at the output node of an opamp because its output current is unknown.

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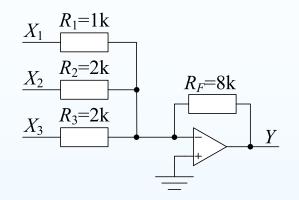
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Nodal Analysis: Do KCL at  $V_+$  and/or  $V_-$  to solve circuit. When analysing a circuit, you never do KCL at the output node of an opamp because its output current is unknown. The only exception is if you have already solved the circuit and you want to find out what the op amp output current is (e.g. to check it is not too high).

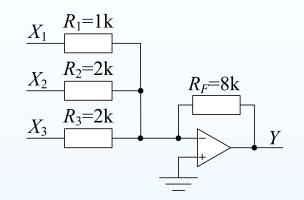
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We can connect several input signals to the inverting amplifier.



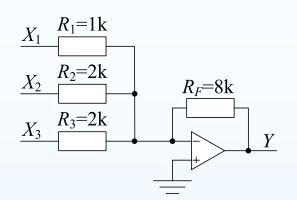
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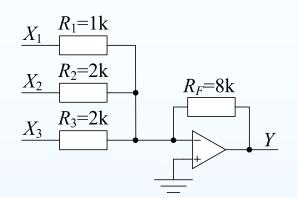
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KCL at 
$$V_{-}$$
 node:  $\frac{0-X_{1}}{R_{1}} + \frac{0-X_{2}}{R_{2}} + \frac{0-X_{3}}{R_{3}} + \frac{0-Y}{R_{F}} = 0$ 

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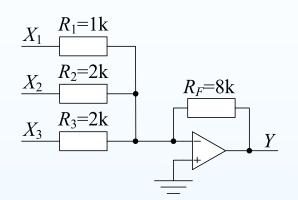
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 $\Rightarrow Y = -\left(\frac{R_{F}}{R_{1}}X_{1} + \frac{R_{F}}{R_{2}}X_{2} + \frac{R_{F}}{R_{3}}X_{3}\right)$ 

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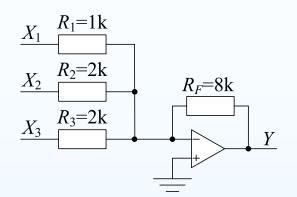


$$\begin{array}{l} \text{CL at } V_{-} \text{ node: } \frac{0-X_{1}}{R_{1}} + \frac{0-X_{2}}{R_{2}} + \frac{0-X_{3}}{R_{3}} + \frac{0-Y}{R_{F}} = 0 \\ \Rightarrow \quad Y = -\left(\frac{R_{F}}{R_{1}}X_{1} + \frac{R_{F}}{R_{2}}X_{2} + \frac{R_{F}}{R_{3}}X_{3}\right) \\ \Rightarrow \quad Y = -\left(8X_{1} + 4X_{2} + 4X_{3}\right). \end{array}$$

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We can connect several input signals to the inverting amplifier.

As before,  $V_{-} = 0$  is a virtual earth due to negative feedback and  $V_{+} = 0$ .



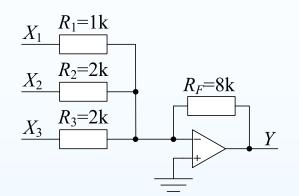
$$\begin{array}{l} \text{KCL at } V_{-} \text{ node: } \frac{0-X_{1}}{R_{1}} + \frac{0-X_{2}}{R_{2}} + \frac{0-X_{3}}{R_{3}} + \frac{0-Y}{R_{F}} = 0 \\ \Rightarrow \quad Y = -\left(\frac{R_{F}}{R_{1}}X_{1} + \frac{R_{F}}{R_{2}}X_{2} + \frac{R_{F}}{R_{3}}X_{3}\right) \\ \Rightarrow \quad Y = -\left(8X_{1} + 4X_{2} + 4X_{3}\right). \end{array}$$

*Y* is a weighted sum of the input voltages with the weight of  $X_i$  equal to  $-\frac{R_F}{R_i} = -G_i R_F$ .

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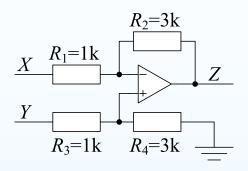
KCL at 
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 $\Rightarrow \quad Y = -\left(8X_1 + 4X_2 + 4X_3\right).$ 

*Y* is a weighted sum of the input voltages with the weight of  $X_i$  equal to  $-\frac{R_F}{R_i} = -G_i R_F$ .

Input Isolation: The current through  $R_1$  equals  $\frac{X_1-0}{R_1}$  which is not affected by  $X_2$  or  $X_3$ . Because  $V_-$  is held at a fixed voltage, the inputs are isolated from each other.

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A 2-input circuit combining inverting and non-inverting amplifiers.

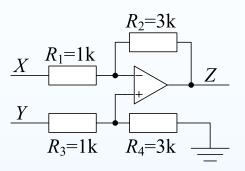


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A 2-input circuit combining inverting and non-inverting amplifiers.

 $Linearity \Rightarrow Z = aX + bY.$ 

Use superposition to find a and b.



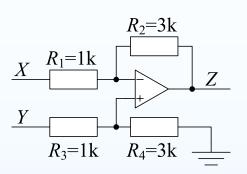
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Find a: Set Y = 0.

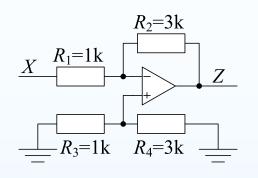


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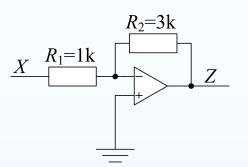
Find *a*: Set Y = 0. KCL at  $V_+$  node  $\Rightarrow V_+ = 0$ .

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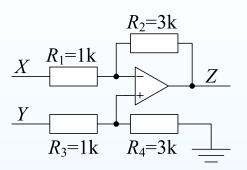
Find *a*: Set Y = 0. KCL at  $V_+$  node  $\Rightarrow V_+ = 0$ . We now have an inverting amplifier, so  $Z = -\frac{R_2}{R_1}X = -3X \Rightarrow a = -3$ .

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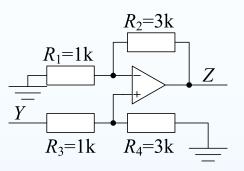
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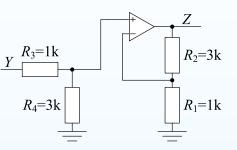
Find b: Set X = 0. We can redraw circuit to make it look more familiar:

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A 2-input circuit combining inverting and non-inverting amplifiers.

 $Linearity \Rightarrow Z = aX + bY.$ 





Find *a*: Set Y = 0. KCL at  $V_+$  node  $\Rightarrow V_+ = 0$ . We now have an inverting amplifier, so  $Z = -\frac{R_2}{R_1}X = -3X \Rightarrow a = -3$ .

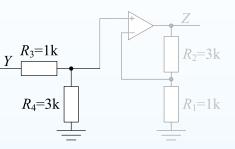
Find *b*: Set X = 0. We can redraw circuit to make it look more familiar: a potential divider followed by a non-inverting amplifier.

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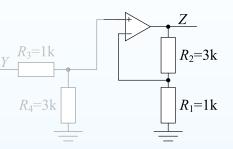
 $R_3$  and  $R_4$  are a potential divider (since current into  $V_+$  equals zero), so  $V_+ = \frac{R_4}{R_3 + R_4} Y = \frac{3}{4} Y$ .

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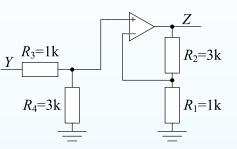
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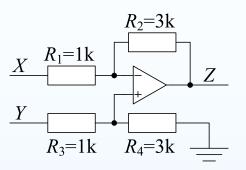
The non-inverting amplifier has a gain of  $\frac{R_1+R_2}{R_1} = 4$ . The combined gain is  $b = \frac{R_4}{R_3+R_4} \times \frac{R_1+R_2}{R_1} = \frac{3}{4} \times 4 = +3$ .

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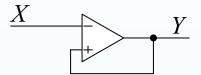
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Combining the two gives Z = 3 (Y - X). The output of a *differential amplifier* is proportional to the difference between its two inputs.

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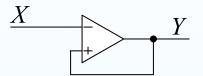
Positive feedback: If op-amp output Y rises then  $(V_+ - V_-)$  will increase. This causes Y to rise even more up to its maximum value (e.g. +14 V).



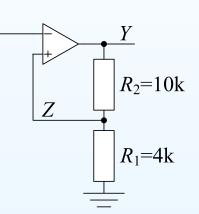
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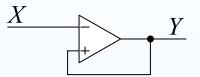
X

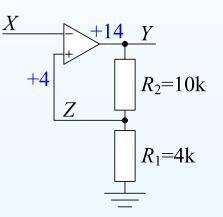


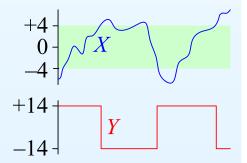
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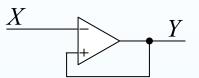


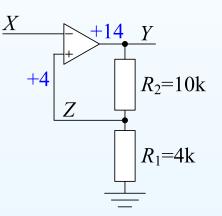


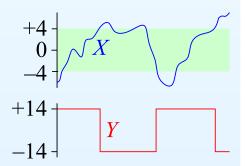
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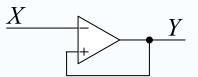


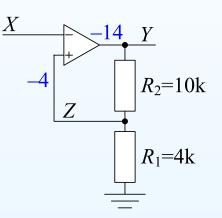


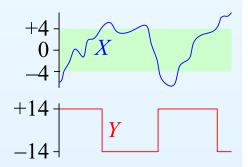
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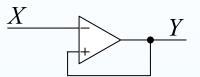


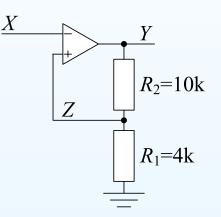


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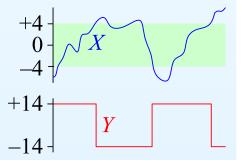
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Negative feedback stabilizes the output to make  $V_+ \simeq V_-$ .

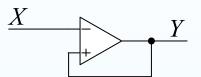
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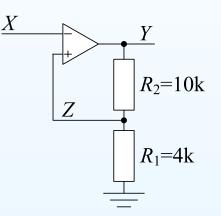


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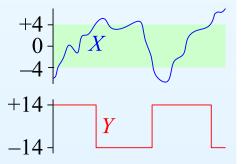




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Positive feedback adjusts the output to maximize  $|V_+ - V_-|$ . Output will switch between its maximum and minimum values, e.g.  $\pm 14 \text{ V}$  (slightly less than the  $\pm 15 \text{ V}$  power supplies).

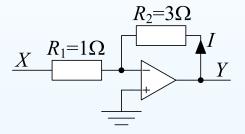
Switching will happen when  $V_+ = V_-$ .



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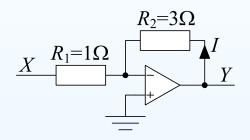


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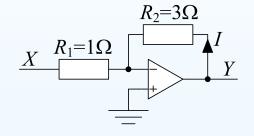


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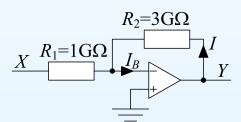
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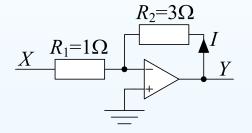
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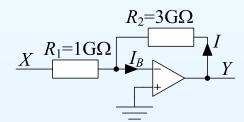
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#### **Choosing Resistor Values**

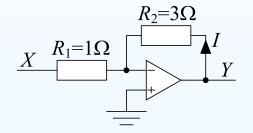
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 $\begin{array}{c} R_2 = 3 G \Omega \\ X R_1 = 1 G \Omega \\ I_B \\ Y \\ I_B \\ I_B \\ Y \\ I_B \\ Y \\ I_B \\ I_B \\ Y \\ I_B \\ I$ 

 $\frac{0-Y}{R_2} + \frac{0-X}{R_1} + I_B = 0 \Rightarrow Y = -\frac{R_2}{R_1}X + I_BR_2 = -3X + 3$  instead of Y = -3X.

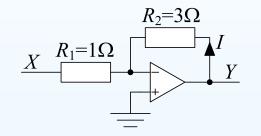
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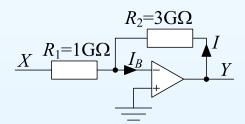
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Within wide limits, the absolute resistor values have little effect. However you should avoid extremes.

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#### • Ideal properties:

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- Ideal properties:
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- Ideal properties:
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  - Infinite gain Ο
  - Do not use KCL at output (except to determine output current). Ο

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For further details see Hayt Ch 6 or Irwin Ch 4.