Digital Electronics
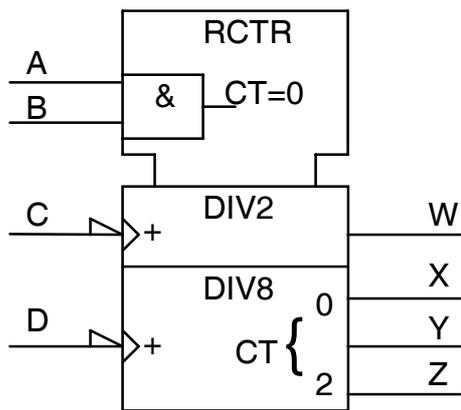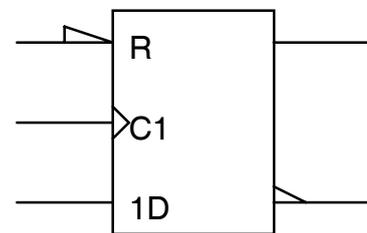
Question Sheet 13

1.** The symbol below represents a commercially available resetable counter. Show how you would make this counter using the D flip-flops (also shown below) and appropriate gates.

2.** Draw the state diagram of the ring counter below assuming that it is initialised such that a) Q3:0 equals 4 and b) Q3:0 equals 3.